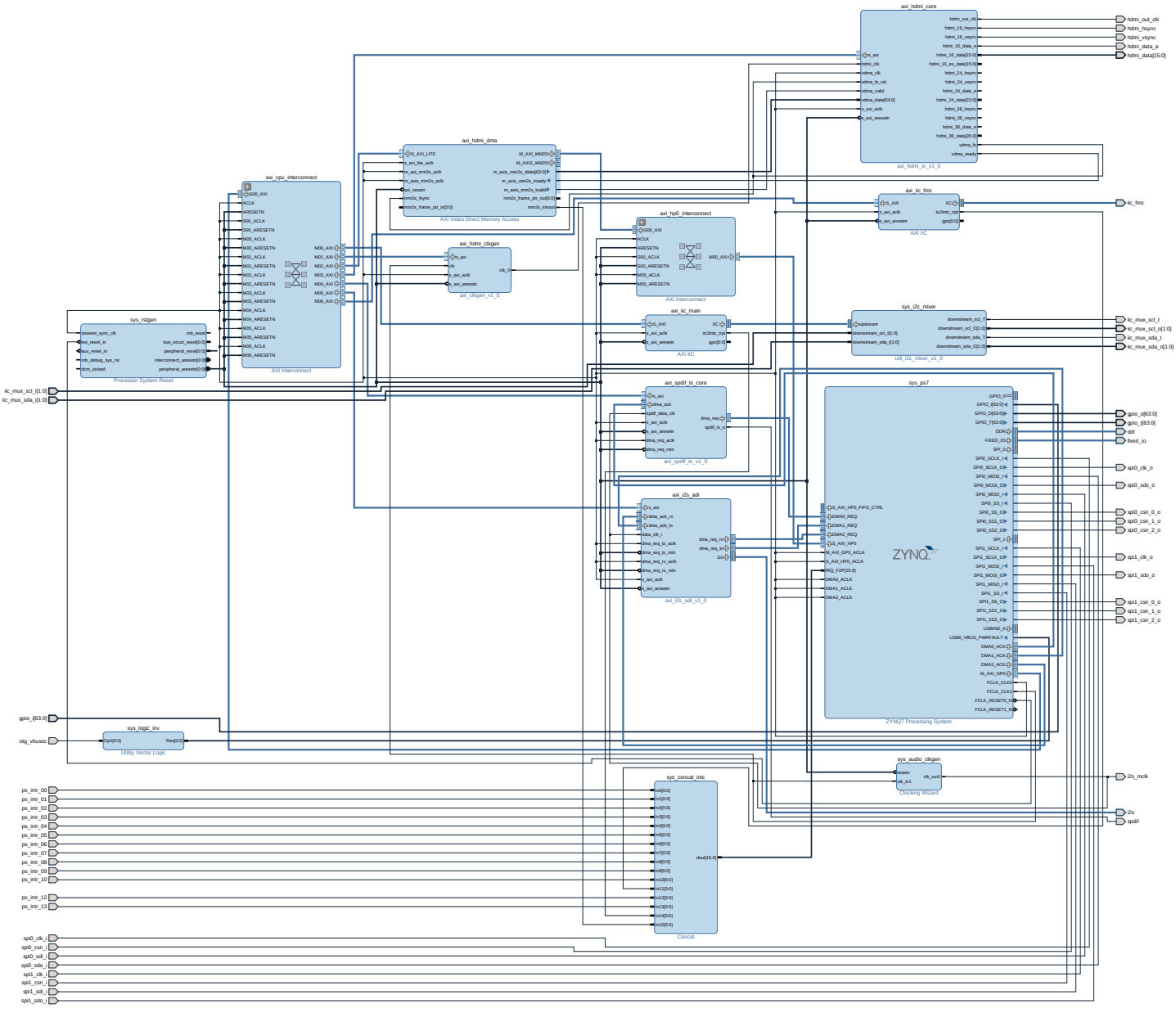
axi_hdmi_core
axi_hdmi_tx_v1_0
axi_hdmi_dma
AXI Video Direct Memory Access
axi_cpu_interconnect
AXI Interconnect
axi_hdmi_clkgen
axi_clkgen_v1_0
axi_hp0_interconnect
AXI Interconnect
axi_iic_fmc
AXI IIC
axi_iic_main
AXI IIC
sys_i2c_mixer
util_i2c_mixer_v1_0
sys_rstgen
Processor System Reset
axi_spdif_tx_core
axi_spdif_tx_v1_0
axi_i2s_adi
axi_i2s_adi_v1_0
sys_ps7
ZYNQ7 Processing System
sys_logic_inv
Utility Vector Logic
sys_concat_intc
Concat
sys_audio_clkgen
Clocking Wizard
hdmi_out_clk
hdmi_hsync
hdmi_vsync
hdmi_data_e
hdmi_data[15:0]
iic_fmc
iic_mux_scl_t
iic_mux_scl_o[1:0]
iic_mux_sda_t
iic_mux_sda_o[1:0]
iic_mux_scl_i[1:0]
iic_mux_sda_i[1:0]
gpio_o[63:0]
gpio_t[63:0]
ddr
fixed_io
spi0_clk_o
spi0_sdo_o
spi0_csn_0_o
spi0_csn_1_o
spi0_csn_2_o
spi1_clk_o
spi1_sdo_o
spi1_csn_0_o
spi1_csn_1_o
spi1_csn_2_o
i2s_mclk
i2s
spdif
gpio_i[63:0]
otg_vbusoc
ps_intr_00
ps_intr_01
ps_intr_02
ps_intr_03
ps_intr_04
ps_intr_05
ps_intr_06
ps_intr_07
ps_intr_08
ps_intr_09
ps_intr_10
ps_intr_12
ps_intr_13
spi0_clk_i
spi0_csn_i
spi0_sdi_i
spi0_sdo_i
spi1_clk_i
spi1_csn_i
spi1_sdi_i
spi1_sdo_i