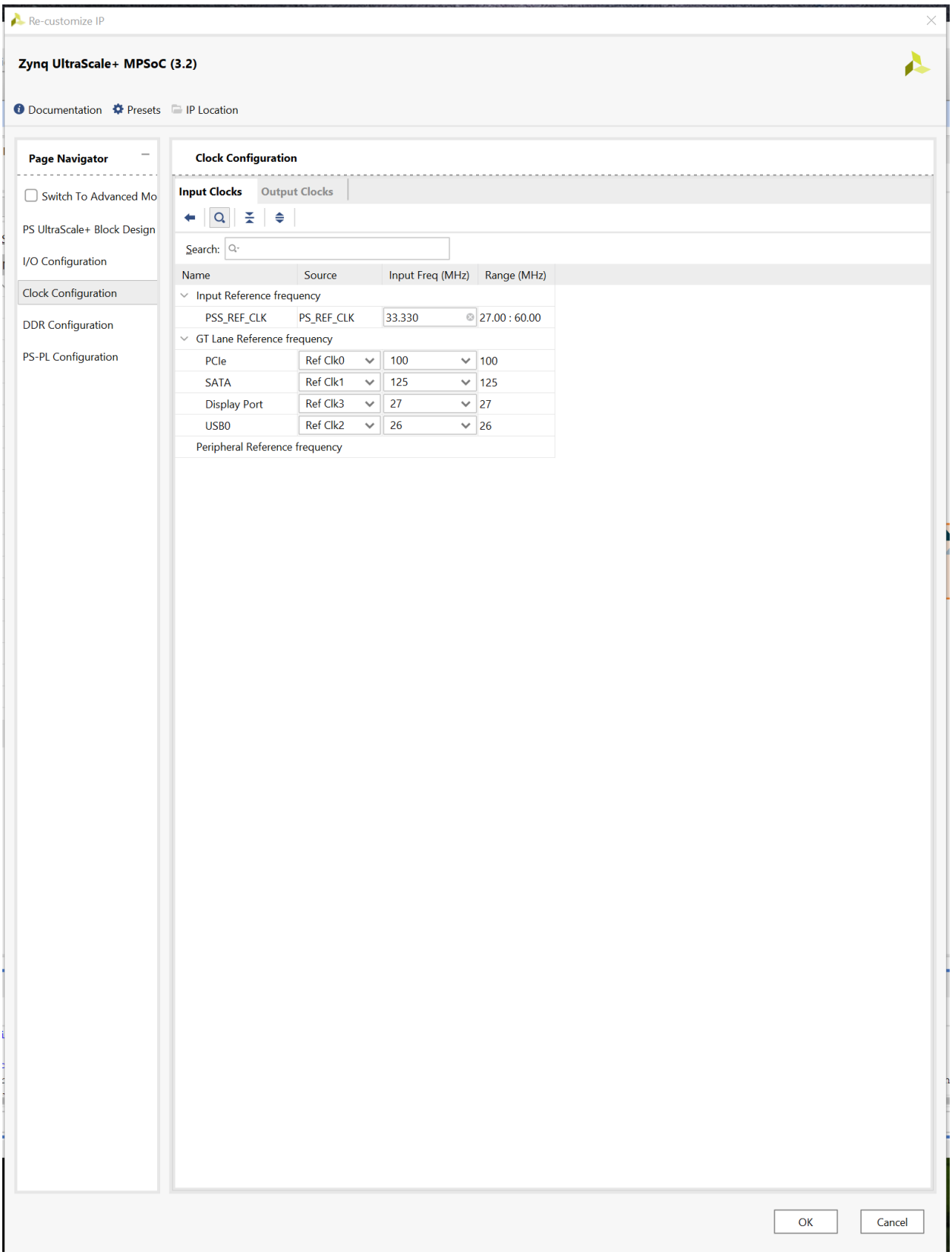
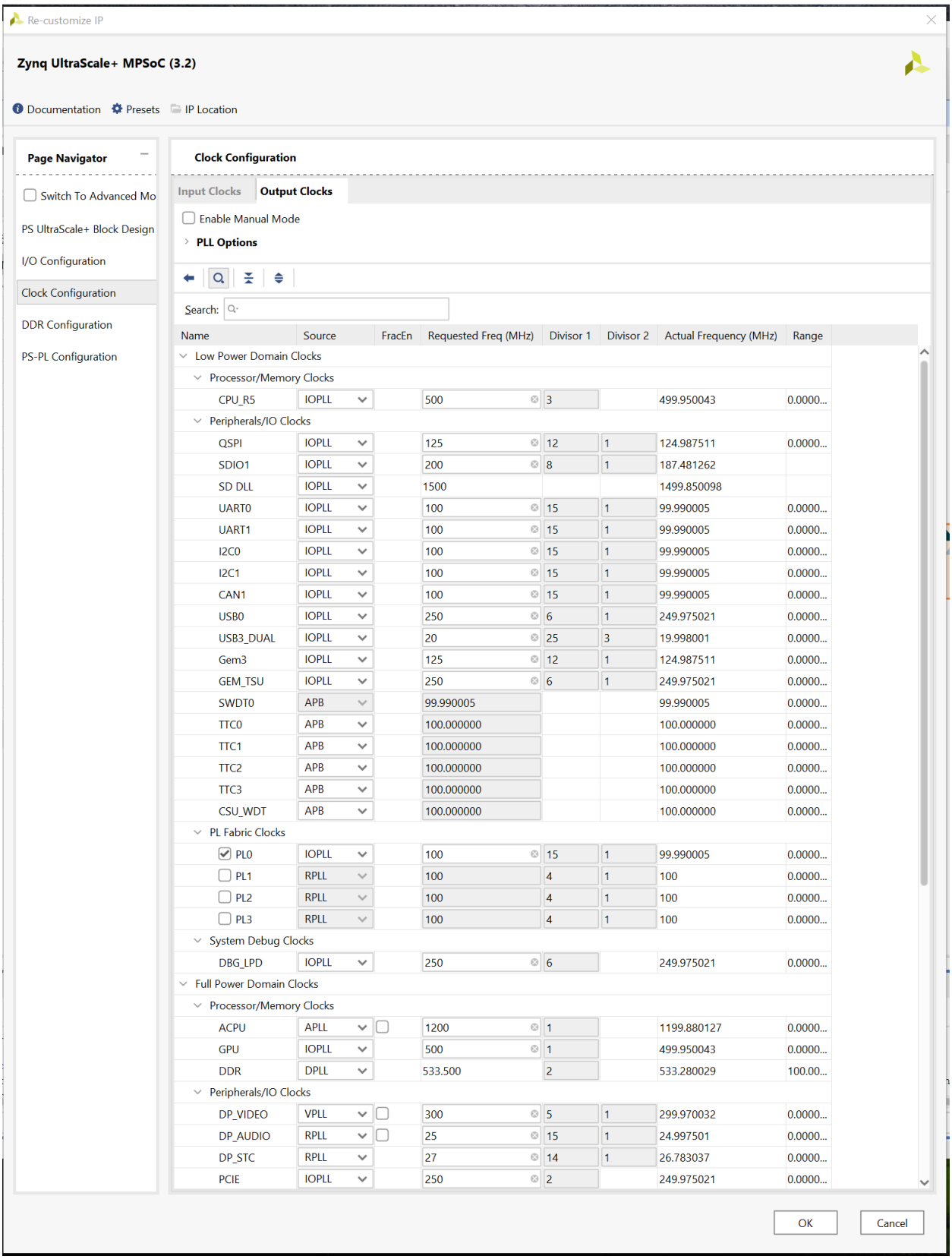
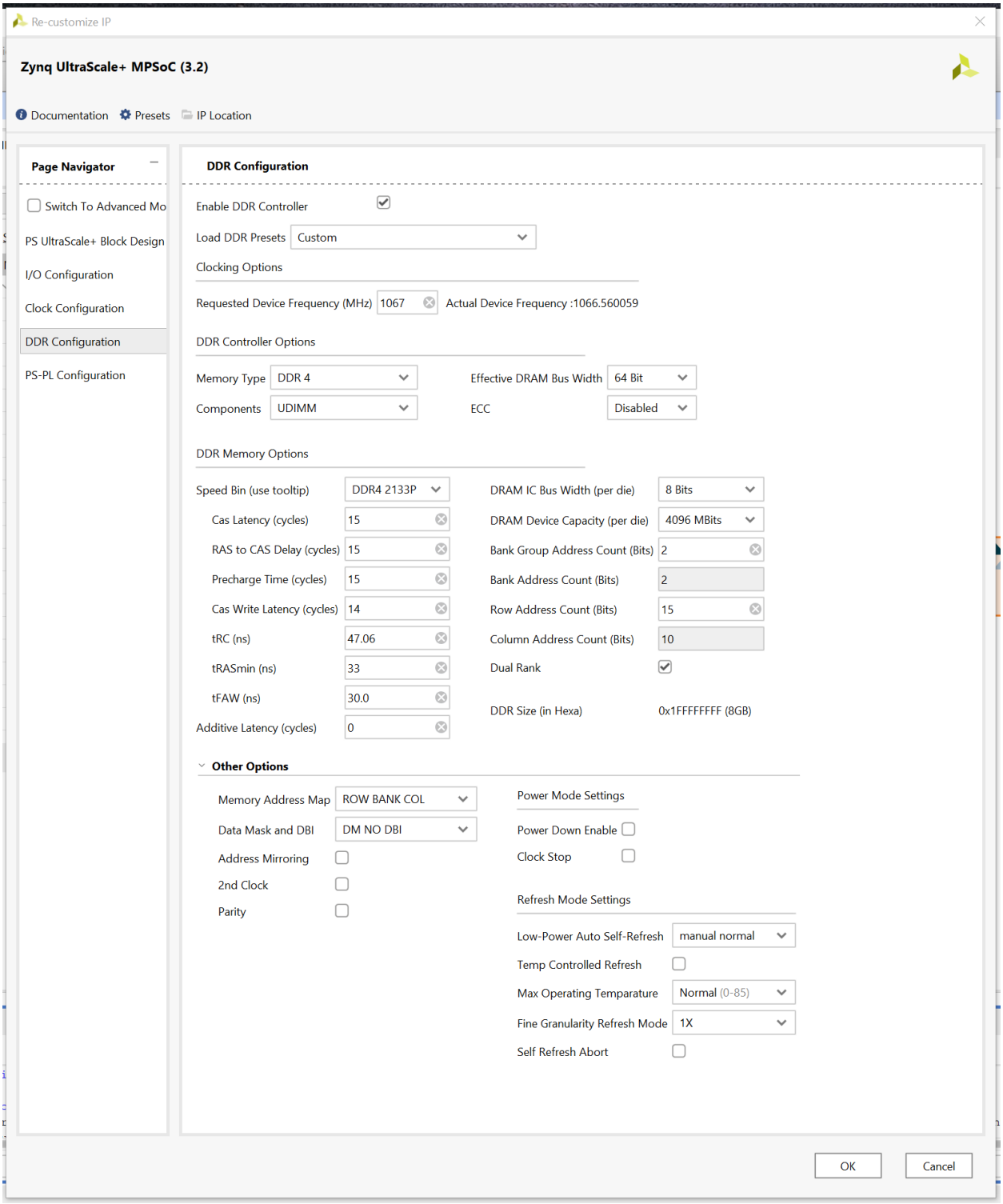

Re-customize IP
Zynq UltraScale+ MPSoC (3.2)
Documentation
Presets
IP Location
Page Navigator
Switch To Advanced Mo
PS UltraScale+ Block Design
I/O Configuration
Clock Configuration
DDR Configuration
PS-PL Configuration
Clock Configuration
Input Clocks
Output Clocks
Search:
Name
Source
Input Freq (MHz)
Range (MHz)
Input Reference frequency
PSS_REF_CLK
PS_REF_CLK
33.330
27.00 : 60.00
GT Lane Reference frequency
PCIe
Ref Clk0
100
100
SATA
Ref Clk1
125
125
Display Port
Ref Clk3
27
27
USB0
Ref Clk2
26
26
Peripheral Reference frequency
OK
Cancel


Re-customize IP
Zynq UltraScale+ MPSoC (3.2)
Documentation
Presets
IP Location
Page Navigator
Switch To Advanced Mo
PS UltraScale+ Block Design
I/O Configuration
Clock Configuration
DDR Configuration
PS-PL Configuration
Clock Configuration
Input Clocks
Output Clocks
Enable Manual Mode
PLL Options
Search:
Name
Source
FracEn
Requested Freq (MHz)
Divisor 1
Divisor 2
Actual Frequency (MHz)
Range
Low Power Domain Clocks
Processor/Memory Clocks
CPU_R5 IOPLL 500 3 499.950043 0.0000...
Peripherals/IO Clocks
QSPI IOPLL 125 12 1 124.987511 0.0000...
SDIO1 IOPLL 200 8 1 187.481262
SD DLL IOPLL 1500 1499.850098
UART0 IOPLL 100 15 1 99.990005 0.0000...
UART1 IOPLL 100 15 1 99.990005 0.0000...
I2C0 IOPLL 100 15 1 99.990005 0.0000...
I2C1 IOPLL 100 15 1 99.990005 0.0000...
CAN1 IOPLL 100 15 1 99.990005 0.0000...
USB0 IOPLL 250 6 1 249.975021 0.0000...
USB3_DUAL IOPLL 20 25 3 19.998001 0.0000...
Gem3 IOPLL 125 12 1 124.987511 0.0000...
GEM_TSU IOPLL 250 6 1 249.975021 0.0000...
SWDT0 APB 99.990005 99.990005 0.0000...
TTC0 APB 100.000000 100.000000 0.0000...
TTC1 APB 100.000000 100.000000 0.0000...
TTC2 APB 100.000000 100.000000 0.0000...
TTC3 APB 100.000000 100.000000 0.0000...
CSU_WDT APB 100.000000 100.000000 0.0000...
PL Fabric Clocks
PL0 IOPLL 100 15 1 99.990005 0.0000...
PL1 RPLL 100 4 1 100 0.0000...
PL2 RPLL 100 4 1 100 0.0000...
PL3 RPLL 100 4 1 100 0.0000...
System Debug Clocks
DBG_LPD IOPLL 250 6 249.975021 0.0000...
Full Power Domain Clocks
Processor/Memory Clocks
ACPU APLL 1200 1 1199.880127 0.0000...
GPU IOPLL 500 1 499.950043 0.0000...
DDR DPLL 533.500 2 533.280029 100.00...
Peripherals/IO Clocks
DP_VIDEO VPLL 300 5 1 299.970032 0.0000...
DP_AUDIO RPLL 25 15 1 24.997501 0.0000...
DP_STC RPLL 27 14 1 26.783037 0.0000...
PCIE IOPLL 250 2 249.975021 0.0000...
OK
Cancel



Screen clipping taken: 4/16/2019 10:52 AM