



Alc\_AxILite\_0

- Med\_Zynq\_Clk\_40M96Hz
- Med\_Zynq\_Clk\_24MHz
- Med\_Zynq\_Clk\_25MHz
- PHY\_J201\_Int\_n
- AlcControl\_Sob
- AlcControl\_Tim\_Frm\_n
- AlcControl\_Tim\_Clk
- AlcControl\_Tim\_Rpt
- Z\_AdDataCk
- Z\_AdDataFrm
- Z\_AdData[0:7]
- Leaf[0]
- Pin[0]
- Ull[4:1]
- TP3[1:1]
- PHY\_J201\_Mdc
- PHY\_J201\_Mdc
- PHY\_J201\_Mdc
- AlcControl\_Hdr\_n
- AlcControl\_Frm\_n
- AlcControl\_Sck
- AlcControl\_Sck
- Z\_AdDataCk
- Z\_AdDataFrm
- Z\_AdData[0:7]
- IsBram\_Kc
- IsBram\_Bv
- IsBram\_Len
- IsBram\_Weq[0]
- IsBram\_Adq[0]
- IsBram\_Deq[0]

blk\_mem\_gen\_0

Block Memory Generator

- BRAM\_PORTA
- BRAM\_PORTB
- addr[31:0]
- data[31:0]
- enb
- rstb
- weq[3:0]

axi\_bram\_ctrl\_0

AXI BRAM Controller

- S\_AXI
- s\_axi\_ack
- s\_axi\_aseqen

xadc\_wiz\_0

XADC Wizard

- ip2inc\_ip[0]
- user\_wmp\_alarm\_out
- vccaux\_alarm\_out
- vccaux\_alarm\_out
- vccaux\_alarm\_out
- vccaux\_alarm\_out
- vccaux\_alarm\_out
- channel\_out[0]
- exc\_out
- alarm\_out
- eos\_out
- busy\_out

axi\_uartlite\_2

AXI UARTlite

- S\_AXI
- s\_axi\_ack
- s\_axi\_aseqen
- interrupt

axi\_uartlite\_1

AXI UARTlite

- S\_AXI
- s\_axi\_ack
- s\_axi\_aseqen
- interrupt

axi\_interconnect\_0

AXI Interconnect

- M00\_AXI
- M01\_AXI
- M02\_AXI
- M03\_AXI
- M04\_AXI
- M05\_AXI
- M06\_AXI
- M07\_AXI
- S00\_AXI
- S01\_AXI
- S02\_AXI
- S03\_AXI
- S04\_AXI
- S05\_AXI
- S06\_AXI
- S07\_AXI
- ARESETN
- S00\_ACLK
- S01\_ACLK
- S02\_ACLK
- S03\_ACLK
- S04\_ACLK
- S05\_ACLK
- S06\_ACLK
- S07\_ACLK
- ARESETN
- S00\_ARESETN
- S01\_ARESETN
- S02\_ARESETN
- S03\_ARESETN
- S04\_ARESETN
- S05\_ARESETN
- S06\_ARESETN
- S07\_ARESETN

proc\_sys\_reset\_0

Processor System Reset

- lowest\_sync\_clk
- rst\_reset\_in
- mb\_reset[0:0]
- bus\_struct\_reset[0:0]
- peripheral\_reset[0:0]
- interconnect\_reset[0:0]
- peripheral\_aseqen[0:0]

processing\_system\_0

ZYNQ

- DDR
- FIXED\_IO
- IIC\_0
- IIC\_1
- IIC\_2
- USB0\_0
- M\_AXI\_GRP0
- TTC0\_WAVE1\_OUT
- TTC0\_WAVE2\_OUT
- FCLK\_CLK0
- FCLK\_RESET0\_N
- M\_AXI\_GRP0\_ACLK
- IRQ\_FZP[0]

xlnconcal\_0

Concat

- in[0:0]
- out[0:0]

ZYNQ Processing System