

Post-PAR Static Timing Report

Sun 5. Nov 21:37:10 2017

Release 14.7 Trace (nt64)

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C:\Xilinx\14.7\ISE_DS\ISE\bin\nt64\unwrapped\trce.exe -intstyle ise -v 3 -s 2

-n 3 -fastpaths -xml adder_32b_clocked1.twx adder_32b_clocked1.ncd -o

adder_32b_clocked1.twr adder_32b_clocked1.pcf

Design file: adder_32b_clocked1.ncd

Physical constraint file: adder_32b_clocked1.pcf

Device,package,speed: xc7k70t,fbg676,C,-2 (PRODUCTION 1.10 2013-10-13)

Report level: verbose report

Environment Variable Effect

NONE No environment variables were set

INFO:Timing:2698 - No timing constraints found, doing default enumeration.

INFO:Timing:3412 - To improve timing, see the Timing Closure User Guide (UG612).

INFO:Timing:2752 - To get complete path coverage, use the unconstrained paths option. All paths that are not constrained will be reported in the unconstrained paths section(s) of the report.

INFO:Timing:3339 - The clock-to-out numbers in this timing report are based on a 50 Ohm transmission line loading model. For the details of this model, and for more information on accounting for different loading conditions, please see the device datasheet.

Data Sheet report:

All values displayed in nanoseconds (ns)

Setup/Hold to clock clk

Source	Max Setup to clk (edge)	Process Corner	Max Hold to clk (edge)	Process Corner	Internal Clock(s)	Clock Phase
a<0>	2.078 (R)	FAST	0.785 (R)	SLOW	clk_BUFGP	0.000
a<1>	1.792 (R)	FAST	1.217 (R)	SLOW	clk_BUFGP	0.000
a<2>	1.824 (R)	FAST	1.220 (R)	SLOW	clk_BUFGP	0.000
a<3>	1.826 (R)	FAST	1.288 (R)	SLOW	clk_BUFGP	0.000
a<4>	1.703 (R)	FAST	1.330 (R)	SLOW	clk_BUFGP	0.000
a<5>	1.613 (R)	FAST	1.274 (R)	SLOW	clk_BUFGP	0.000
a<6>	1.606 (R)	FAST	1.292 (R)	SLOW	clk_BUFGP	0.000
a<7>	1.549 (R)	FAST	1.281 (R)	SLOW	clk_BUFGP	0.000
a<8>	1.571 (R)	FAST	1.301 (R)	SLOW	clk_BUFGP	0.000
a<9>	1.541 (R)	FAST	1.265 (R)	SLOW	clk_BUFGP	0.000
a<10>	1.598 (R)	FAST	1.253 (R)	SLOW	clk_BUFGP	0.000
a<11>	1.619 (R)	FAST	1.222 (R)	SLOW	clk_BUFGP	0.000
a<12>	1.471 (R)	FAST	1.373 (R)	SLOW	clk_BUFGP	0.000
a<13>	1.629 (R)	FAST	1.291 (R)	SLOW	clk_BUFGP	0.000
a<14>	1.455 (R)	FAST	1.380 (R)	SLOW	clk_BUFGP	0.000
a<15>	1.596 (R)	FAST	0.989 (R)	SLOW	clk_BUFGP	0.000
a<16>	1.606 (R)	FAST	1.100 (R)	SLOW	clk_BUFGP	0.000
a<17>	1.744 (R)	FAST	0.812 (R)	SLOW	clk_BUFGP	0.000
a<18>	1.443 (R)	FAST	1.186 (R)	SLOW	clk_BUFGP	0.000
a<19>	1.594 (R)	FAST	1.051 (R)	SLOW	clk_BUFGP	0.000
a<20>	1.561 (R)	FAST	1.132 (R)	SLOW	clk_BUFGP	0.000
a<21>	1.456 (R)	FAST	1.078 (R)	SLOW	clk_BUFGP	0.000
a<22>	1.342 (R)	FAST	1.270 (R)	SLOW	clk_BUFGP	0.000
a<23>	1.374 (R)	FAST	1.119 (R)	SLOW	clk_BUFGP	0.000
a<24>	1.413 (R)	FAST	1.179 (R)	SLOW	clk_BUFGP	0.000
a<25>	1.475 (R)	FAST	1.128 (R)	SLOW	clk_BUFGP	0.000
a<26>	1.237 (R)	FAST	1.369 (R)	SLOW	clk_BUFGP	0.000
a<27>	1.209 (R)	FAST	1.334 (R)	SLOW	clk_BUFGP	0.000
a<28>	1.290 (R)	FAST	1.464 (R)	SLOW	clk_BUFGP	0.000
a<29>	1.222 (R)	FAST	1.379 (R)	SLOW	clk_BUFGP	0.000
a<30>	1.232 (R)	FAST	1.037 (R)	SLOW	clk_BUFGP	0.000
a<31>	1.029 (R)	FAST	1.055 (R)	SLOW	clk_BUFGP	0.000
b<0>	1.965 (R)	FAST	0.967 (R)	SLOW	clk_BUFGP	0.000
b<1>	1.565 (R)	FAST	1.512 (R)	SLOW	clk_BUFGP	0.000
b<2>	1.410 (R)	FAST	1.597 (R)	SLOW	clk_BUFGP	0.000
b<3>	1.493 (R)	FAST	1.519 (R)	SLOW	clk_BUFGP	0.000
b<4>	1.445 (R)	FAST	1.665 (R)	SLOW	clk_BUFGP	0.000
b<5>	1.382 (R)	FAST	1.595 (R)	SLOW	clk_BUFGP	0.000
b<6>	1.263 (R)	FAST	1.771 (R)	SLOW	clk_BUFGP	0.000
b<7>	1.427 (R)	FAST	1.448 (R)	SLOW	clk_BUFGP	0.000
b<8>	1.246 (R)	FAST	1.736 (R)	SLOW	clk_BUFGP	0.000
b<9>	1.200 (R)	FAST	1.714 (R)	SLOW	clk_BUFGP	0.000
b<10>	1.321 (R)	FAST	1.623 (R)	SLOW	clk_BUFGP	0.000
b<11>	1.210 (R)	FAST	1.574 (R)	SLOW	clk_BUFGP	0.000
b<12>	1.314 (R)	FAST	1.552 (R)	SLOW	clk_BUFGP	0.000
b<13>	1.155 (R)	FAST	1.666 (R)	SLOW	clk_BUFGP	0.000
b<14>	1.197 (R)	FAST	1.686 (R)	SLOW	clk_BUFGP	0.000

b<15>	1.248 (R)	FAST	1.469 (R)	SLOW	clk_BUF	0.000
b<16>	1.128 (R)	FAST	1.719 (R)	SLOW	clk_BUF	0.000
b<17>	1.265 (R)	FAST	1.383 (R)	SLOW	clk_BUF	0.000
b<18>	1.232 (R)	FAST	1.466 (R)	SLOW	clk_BUF	0.000
b<19>	1.255 (R)	FAST	1.447 (R)	SLOW	clk_BUF	0.000
b<20>	1.180 (R)	FAST	1.409 (R)	SLOW	clk_BUF	0.000
b<21>	1.233 (R)	FAST	1.326 (R)	SLOW	clk_BUF	0.000
b<22>	1.321 (R)	FAST	1.283 (R)	SLOW	clk_BUF	0.000
b<23>	1.317 (R)	FAST	1.127 (R)	SLOW	clk_BUF	0.000
b<24>	1.240 (R)	FAST	1.355 (R)	SLOW	clk_BUF	0.000
b<25>	1.241 (R)	FAST	1.292 (R)	SLOW	clk_BUF	0.000
b<26>	1.373 (R)	FAST	1.156 (R)	SLOW	clk_BUF	0.000
b<27>	1.445 (R)	FAST	1.027 (R)	SLOW	clk_BUF	0.000
b<28>	1.222 (R)	FAST	1.316 (R)	SLOW	clk_BUF	0.000
b<29>	1.258 (R)	FAST	1.341 (R)	SLOW	clk_BUF	0.000
b<30>	1.320 (R)	FAST	0.878 (R)	SLOW	clk_BUF	0.000
b<31>	1.278 (R)	FAST	0.694 (R)	SLOW	clk_BUF	0.000

Clock clk to Pad

Destination	Max (slowest) clk (edge) to PAD	Process Corner	Min (fastest) clk (edge) to PAD	Process Corner	Internal Clock(s)	Clock Phase
s<0>	6.871 (R)	SLOW	3.048 (R)	FAST	clk_BUF	0.000
s<1>	6.900 (R)	SLOW	3.077 (R)	FAST	clk_BUF	0.000
s<2>	6.904 (R)	SLOW	3.081 (R)	FAST	clk_BUF	0.000
s<3>	6.872 (R)	SLOW	3.050 (R)	FAST	clk_BUF	0.000
s<4>	6.870 (R)	SLOW	3.048 (R)	FAST	clk_BUF	0.000
s<5>	6.901 (R)	SLOW	3.080 (R)	FAST	clk_BUF	0.000
s<6>	6.900 (R)	SLOW	3.079 (R)	FAST	clk_BUF	0.000
s<7>	6.869 (R)	SLOW	3.048 (R)	FAST	clk_BUF	0.000
s<8>	6.870 (R)	SLOW	3.050 (R)	FAST	clk_BUF	0.000
s<9>	6.890 (R)	SLOW	3.070 (R)	FAST	clk_BUF	0.000
s<10>	6.894 (R)	SLOW	3.074 (R)	FAST	clk_BUF	0.000
s<11>	6.895 (R)	SLOW	3.075 (R)	FAST	clk_BUF	0.000
s<12>	6.894 (R)	SLOW	3.074 (R)	FAST	clk_BUF	0.000
s<13>	6.869 (R)	SLOW	3.048 (R)	FAST	clk_BUF	0.000
s<14>	6.869 (R)	SLOW	3.048 (R)	FAST	clk_BUF	0.000
s<15>	6.904 (R)	SLOW	3.083 (R)	FAST	clk_BUF	0.000
s<16>	6.903 (R)	SLOW	3.082 (R)	FAST	clk_BUF	0.000
s<17>	6.878 (R)	SLOW	3.057 (R)	FAST	clk_BUF	0.000
s<18>	6.879 (R)	SLOW	3.057 (R)	FAST	clk_BUF	0.000
s<19>	6.907 (R)	SLOW	3.084 (R)	FAST	clk_BUF	0.000
s<20>	6.901 (R)	SLOW	3.078 (R)	FAST	clk_BUF	0.000
s<21>	6.877 (R)	SLOW	3.054 (R)	FAST	clk_BUF	0.000
s<22>	6.879 (R)	SLOW	3.056 (R)	FAST	clk_BUF	0.000
s<23>	6.894 (R)	SLOW	3.071 (R)	FAST	clk_BUF	0.000
s<24>	6.889 (R)	SLOW	3.066 (R)	FAST	clk_BUF	0.000
s<25>	6.869 (R)	SLOW	3.046 (R)	FAST	clk_BUF	0.000
s<26>	6.866 (R)	SLOW	3.042 (R)	FAST	clk_BUF	0.000
s<27>	6.887 (R)	SLOW	3.064 (R)	FAST	clk_BUF	0.000
s<28>	6.889 (R)	SLOW	3.066 (R)	FAST	clk_BUF	0.000
s<29>	6.880 (R)	SLOW	3.057 (R)	FAST	clk_BUF	0.000
s<30>	6.871 (R)	SLOW	3.048 (R)	FAST	clk_BUF	0.000
s<31>	6.902 (R)	SLOW	3.078 (R)	FAST	clk_BUF	0.000
s<32>	6.886 (R)	SLOW	3.062 (R)	FAST	clk_BUF	0.000

Analysis completed Sun Nov 05 21:36:00 2017

Trace Settings:

Trace Settings
Peak Memory Usage: 579 MB